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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/826 571	04/05/2001	Anthonius Bakker	PHA80.003	3526

7590

01/30/2003

Attention: Brian J. Wieghaus Philips Electronics North America Corporation

580 White Plains Road Tarrytown, NY 10591-5190 EXAMINER

CHOE, HENRY

ART UNIT PAPER NUMBER

2817

DATE MAILED: 01/30/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No. 09/826,571

Applicant(s)

Bakker

Examiner

Henry Choe

Art Unit **2817**

	on the cover sheet with the correspondence address				
Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.					
 Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In mailing date of this communication. 	i no event, however, may a reply be timely filed after SIX (6) MONTHS from the				
 If the period for reply specified above is less than thirty (30) days, a reply within t If NO period for reply is specified above, the maximum statutory period will apply 	and will expire SIX (6) MONTHS from the mailing date of this communication.				
 Failure to reply within the set or extended period for reply will, by statute, cause t Any reply received by the Office later than three months after the mailing date of earned patent term adjustment. See 37 CFR 1.704(b). 	the application to become ABANDONED (35 U.S.C. § 133).				
earned patent term adjustment. See 37 CFR 1.704(b). Status					
1) X Responsive to communication(s) filed on Nov 8, 20	002				
2a) ☐ This action is FINAL . 2b) ☑ This act	tion is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11; 453 O.G. 213.					
Disposition of Claims					
4) 💢 Claim(s) <u>1-17</u>	is/are pending in the application.				
4a) Of the above, claim(s)	is/are withdrawn from consideration.				
5) Claim(s)	is/are allowed.				
6) X Claim(s) 1, 4, 6, 8, 9, 12, 15, and 17	is/are rejected.				
7) X Claim(s) 2, 3, 5, 7, 10, 11, 13, 14, and 16	is/are objected to.				
8) Claims	are subject to restriction and/or election requirement.				
Application Papers					
9) The specification is objected to by the Examiner.					
10) The drawing(s) filed on is/are a) accepted or b) objected to by the Examiner.					
Applicant may not request that any objection to the c	drawing(s) be held in abeyance. See 37 CFR 1.85(a).				
11) The proposed drawing correction filed on	is: a) \square approved b) \square disapproved by the Examiner.				
If approved, corrected drawings are required in reply to this Office action.					
12) The oath or declaration is objected to by the Examiner.					
Priority under 35 U.S.C. §§ 119 and 120					
13) Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a) All b) Some* c) None of:					
1. Certified copies of the priority documents have been received.					
2. Certified copies of the priority documents have	2. Certified copies of the priority documents have been received in Application No				
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).					
*See the attached detailed Office action for a list of th					
14) Acknowledgement is made of a claim for domestic					
a) The translation of the foreign language provisional application has been received.					
15) Acknowledgement is made of a claim for domestic	priority under 35 U.S.C. §§ 120 and/or 121.				
Attachment(s)					
1) Notice of References Cited (PTO-892)	4) Interview Summary (PTO-413) Paper No(s).				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s).	5) Notice of Informal Patent Application (PTO-152)				
3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 6) Other:					

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 4, 6, 8, 9, 12, 15 and 17 are rejected under 35 U.S.C. 102(b) as being anticipated by Farrow (Fig. 3).

Farrow (Fig. 3) discloses an amplifier circuit comprising an amplifier (12, 26, 66, 76) which produces a DC offset and noise and having an input (24) for receiving an input signal (INPUT SIGNAL) and an output (82) for delivering an output signal (a signal going out of 82), means (62, 69, 72) for reducing the DC offset and noise produced by the amplifier (12, 26, 66, 76), feedback means (90, 92) which further reduces the DC offset produced by the amplifier (12, 26, 66, 76) and wherein the feedback means for further reducing the DC offset produced by the amplifier includes switching means (92), and an adder circuit (88).

Allowable Subject Matter

3. Claims 2, 3, 5, 7, 10, 11, 13, 14 and 16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

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Reasons for Allowance

4. The following is a statement of reasons for the indication of allowable subject matter:

Regarding claims 2 and 10, the closest prior art of record, Farrow (Fig. 3) does not disclose the

following limitation: an output chopper synchronized with the input chopper. Regarding claims 5.

7 and 13, means for adding back the sampled output signal to the amplifier comprises an analog to

digital converter with an input coupled to the output of the circuit.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure.

Patent number (5,585,756) is the amplifier circuit with the dc offset circuit.

6. Any inquiry concerning this communication or earlier communications from the examiner

should be directed to Henry Choe whose telephone number is (703) 305-0576. If attempts to

reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bob Pascal, can be

reached on (703) 308-4909.

Name: Henry Choe

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